

REV.	ECN NO OR DESCRIPTION	REVISED	DATE
△	PROPOSE ONLY	ALLEN	2015.05.23

NOTES:

1. MATERIAL:

SEE TABLE(Housing can't add any recycled material)

2. SPECIALITY:

- 2.1 Rated current:1.0A
- 2.2 Rated voltage:30V
- 2.3 Contact Resistance:50mohm MAX
- 2.4 Insulation Resistance:1000Mohm MIN 500V DC
- 2.5 Dielectric withstanding voltage: 500V DC.
- 2.6 Solder ability:250±5℃, 10±0.5s.
- 2.7 Durability:3000 Cycles
- 2.8 Mating and Unmating force
Insertion Force 10N Max; Withdrawal Fore 2N Min
- 2.9 Operating condition:
Temperature-40℃~+85℃; Humidity 80% R.H MAX

3.D/C AREA DESCRIBE: xxxxxx

YEARS:2014=4
WEEK:01...56
DAY:1=MON,2=TUN...
Line:A/B/C... line
DAY/night shift:
A=Day shift
B=Night shift

4.COPLANARITY: BEFORE IR 0.08mm Max AFTER IR 0.08 mm Max

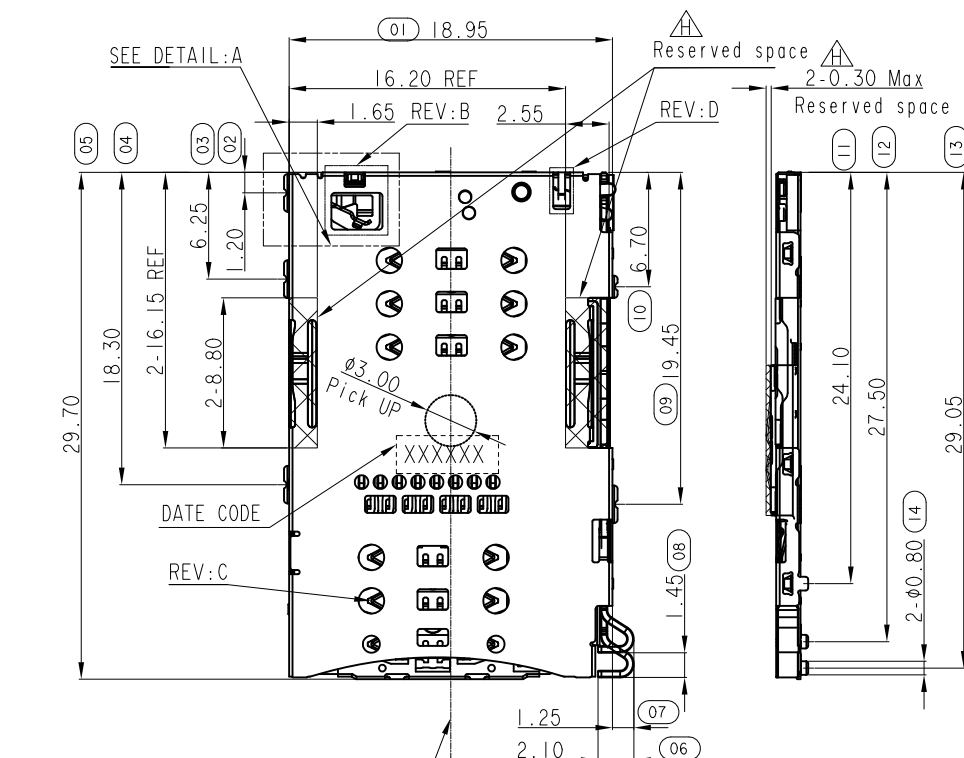
5.PLEASE CONTACT KRCONN SALES REPRESENTATIVE TO VERIFY PRODUCT DETAILS & AVAILABILITY

6	CD PIN		1PCS	C5240
5	BAR		1PCS	SUS304-3/4H
4	LEVER		1PCS	SUS301-1/2H
3	SHELL		1PCS	SUS304-3/4H
2	TERMINAL		1SET	C5240
1	HOUSING		1PCS	LCP BLACK
NO.	PART NAME	PART NO.	QUANTITY	COLOR/MATERIAL

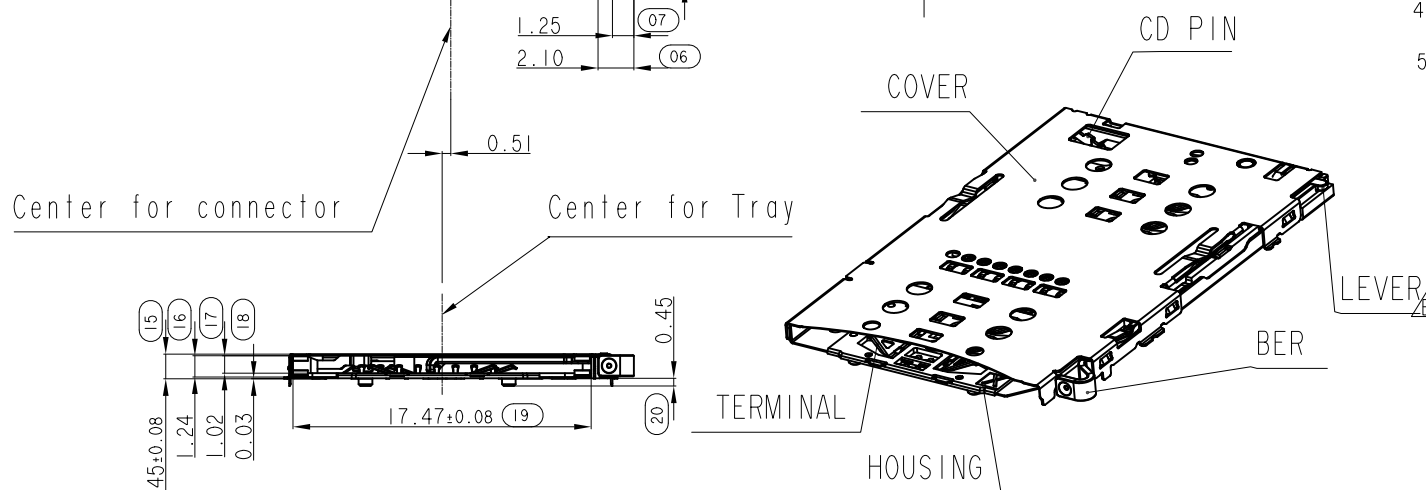
UNLESS OTHERWISE SPECIFIED TOLERANCES		深圳市顺宏康科技有限公司			
DECIMALS:	ANGLES:	TITLE	1.45H Micro/Nano/Ht 3in2 (Pin Push)Conn.Assem.		
X :±0.5	X :±2°	DWN	ALLEN	PART NO. TS0PL-140020-04G	
X.X :±0.20	X.X :±1°	CHKD	KEVIN	SCALE:1:1	UNIT: mm
X.XX :±0.10		APVD	ERIC.W	SIZE: A3	SHEET:10F 1
		CUSTOMER COPY			

TABLE

CONNECTOR	PART NO:	PLATING
MICRO SIM+MICRO SD/Nano SIM CONNECTOR(20pin) 1.45mm HEIGHT	TS0PL-140020-04G	Shell Solder: G/F. 30u"NI PLATING CONTACT: 5u"Au·solder: 1u"Au· 60u"NI PLATING HOUSING(No add any recycling material)



DETAIL: A
SCALE: 2:1



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Micro SIM Circuit Description:

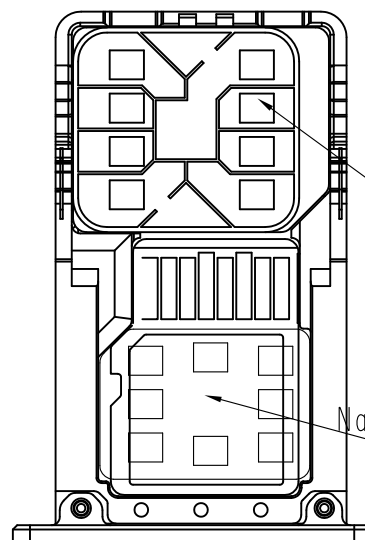
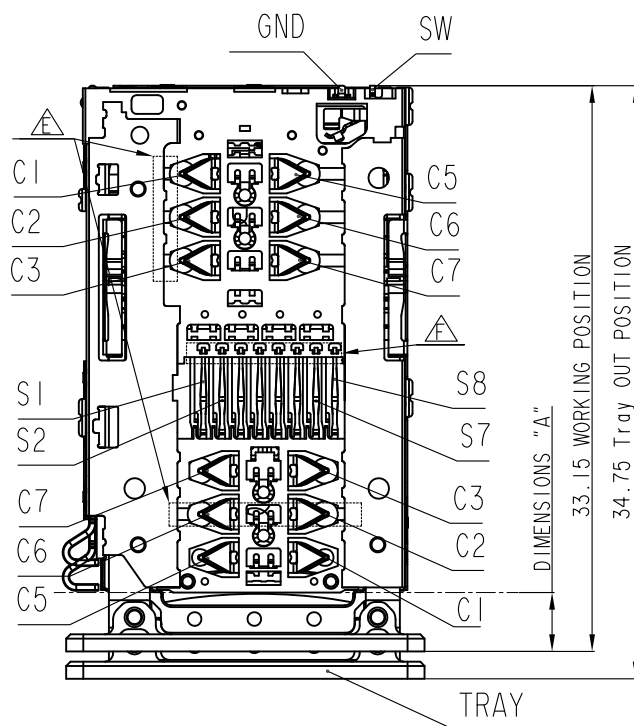
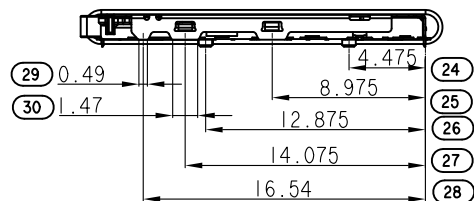
PIN No.	FUNCTION
C1	VCC
C2	RST
C3	CLK
C4	RESERVED
C5	GND
C6	VPP
C7	I/O
C8	RESERVED

Nano SIM Circuit Description:

PIN No.	FUNCTION
C1	VCC
C2	RST
C3	CLK
C4	RESERVED
C5	GND
C6	VPP
C7	I/O
C8	RESERVED

Micro SD Circuit Description:

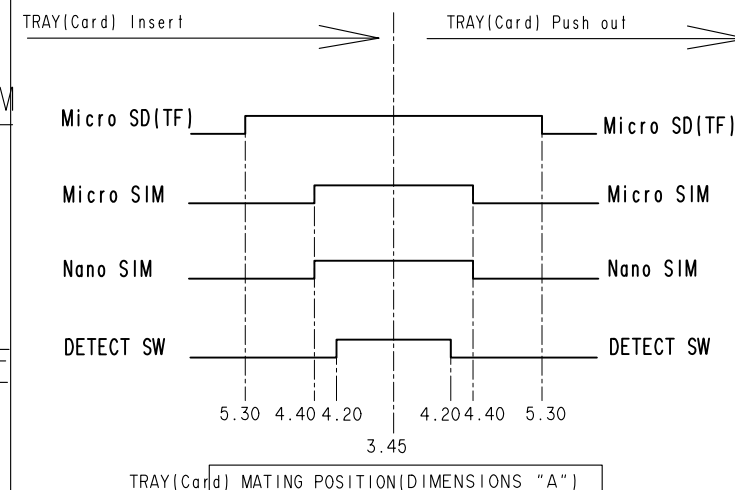
PIN No.	FUNCTION
S1	DAT2
S2	CT/DAT3
S3	CMD
S4	VDD
S5	CLK
S6	VSS
S7	DAT0
S8	DAT1



Micro SIM

Nano SIM/TF

Contact time sequence:



SW Circuit Description for Connector:

CARD INSERTION STATE	DETECT SWITCH CIRCUIT STATE	SCHEMATIC
TRAY UNMATED	OPENED	DETECT PIN [SW] — GND
TRAY MATED	CLOSED	DETECT PIN [SW] — GND

UNLESS OTHERWISE
SPECIFIED TOLERANCES

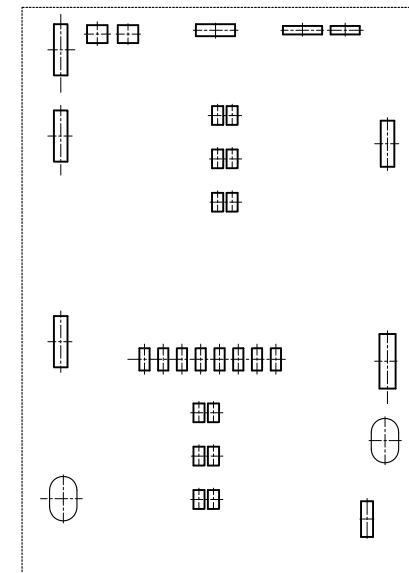
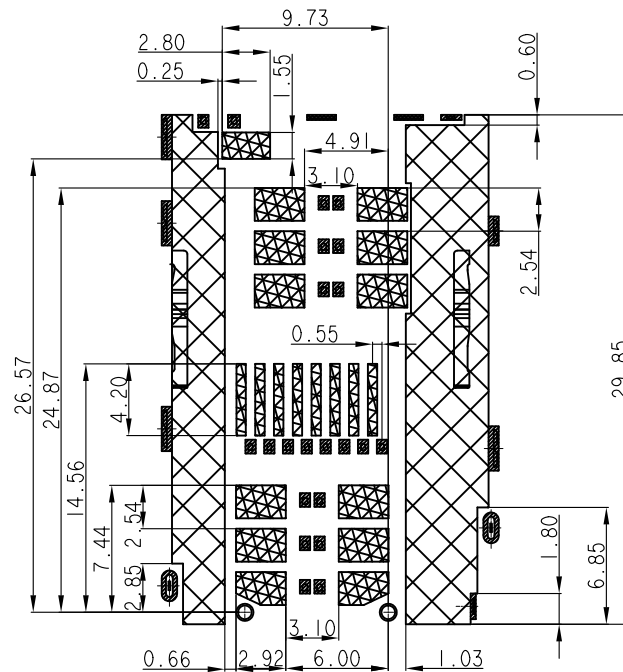
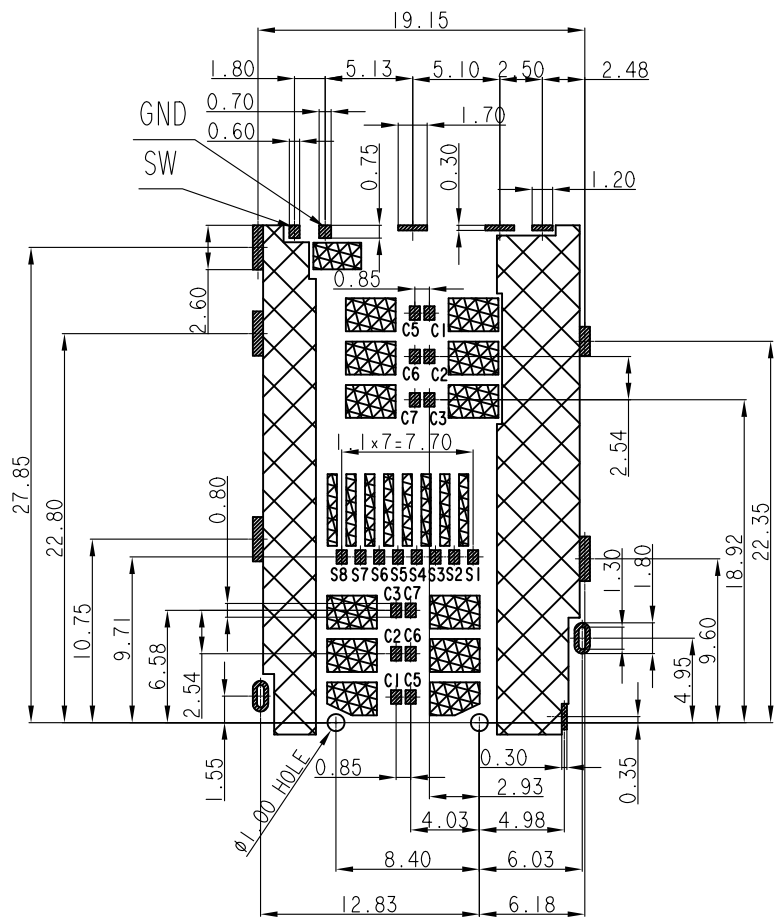
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X.XX :±0.10	

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DWN	ALLEN	PART NO. TSOPL-140020-04G			
CHKD	KEVIN	SCALE:1:1	UNIT: mm		
APVD	ERIC.W	SIZE: A3	SHEET:10F 1	REV: A	

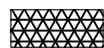
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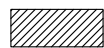


RECOMMENDED MATAL MASK HOLE
RECOMMENDED MATAL MASK T=0.12MM

RECOMMENDED PCB LAYOUT TOP VIEW (TOLERANCE: ±0.05)



THESE AREA SHOULD BE FREE OF
ANY CONDUCTIVE TRACES.



SMT SOLDER AREA.



KEEP OUT AREA(No Circuit)

UNLESS OTHERWISE
SPECIFIED TOLERANCES

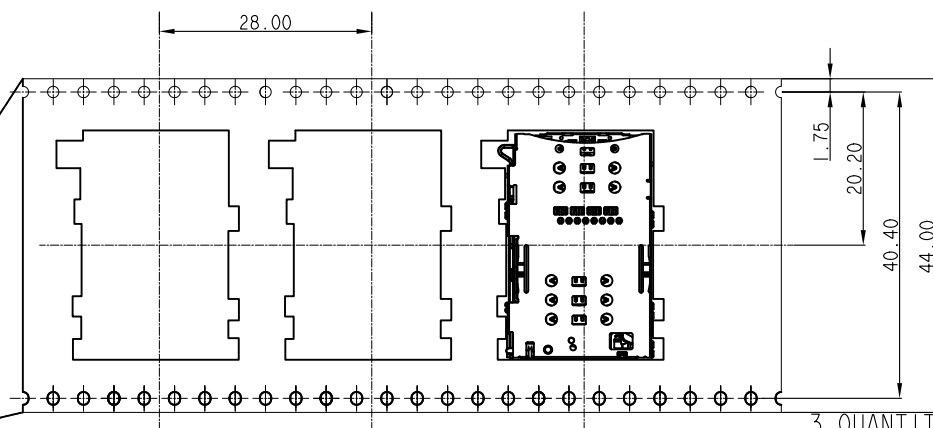
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X.XX : ±0.10	

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TITLE	1.45H Micro/Nano 11 3in2 (Pin Push)Conn.Assem.			
DWN	ALLEN	PART NO. TSOPL-140020-04G		
CHKD	KEVIN	SCALE: 1:1	UNIT: mm	
APVD	ERIC.W	SIZE: A3	SHEET: 10F 1	REV: A

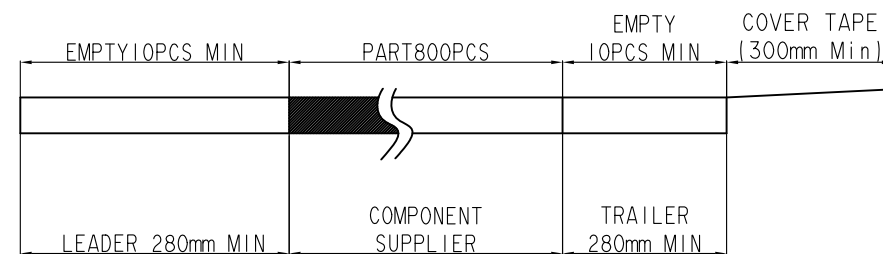
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NOTES

1. NUMBER OF CONNECTORS:800PCS/REEL
2. LEAD TAPE LENGTH



3. QUANTITY: 4000PCS/CARTON

4. WEIGHT :

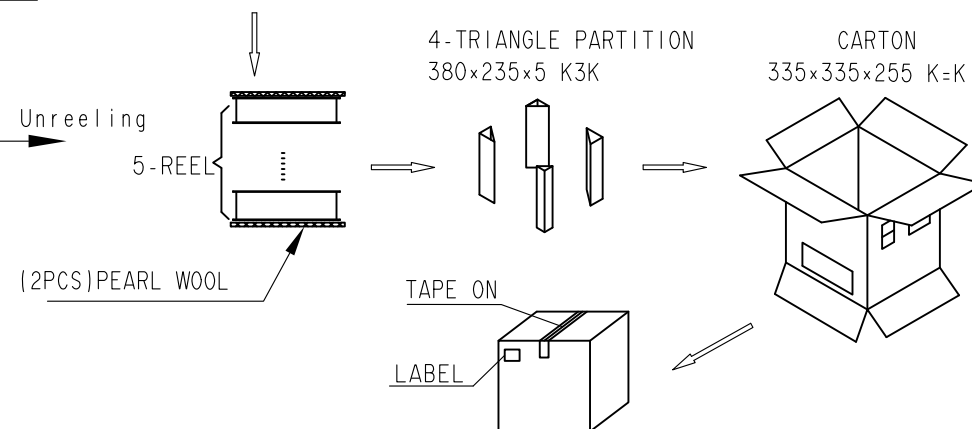
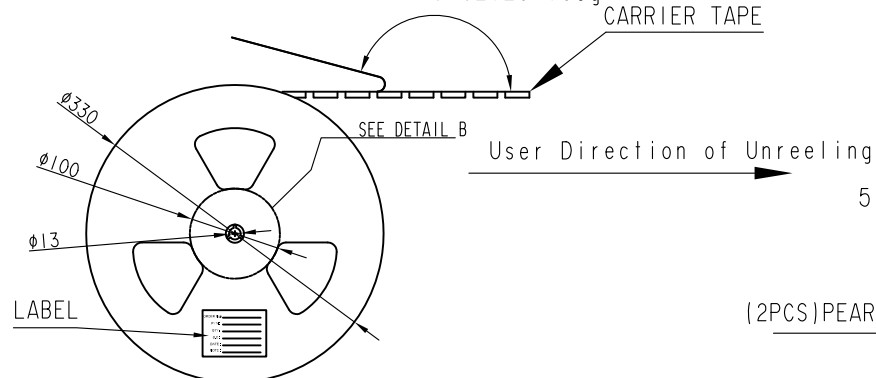
4.1 NET WEIGHT : 3.80Kg/CARTON

4.2 GROSS WEIGHT : 7.20Kg/CARTON

COVER TAPE PEELING ANGLE: 165°-180°

PEELING FORCE: 20-130g

g CARRIER TAPE



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SPECIFIED TOLERANCES

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X.X :±0.20	X.X :±1°	CHKD KEVIN	SCALE: 1:1	UNIT: mm	
X.XX :±0.10		APVD ERIC.W	SIZE: A3	SHEET: 1 OF 1	REV: A

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TITLE	1.45H Micro/Nano/1f 3in2 (Pin Push)Conn.Assem.			
DWN	ALLEN	PART NO. TSOPL-140020-04G		
CHKD	KEVIN	SCALE:1:1	UNIT: mm	
APVD	ERIC.W	SIZE: A3	SHEET:10F 1	REV: A

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